

**GOVERNMENT POLYTECHNIC, SAMBALPUR, RENGALI**

**DEPARTMENT OF E&TC ENGINEERING**

**LESSON PLAN**

**SUBJECT- MICROPROCESSOR & MICROCONTROLLER (TH-3)**

**SEMESTER-4<sup>TH</sup>**

**NAME OF THE FACULTY- MS. LOPAMUDRA BHOI**

**TOTAL NO. OF PERIODS-75(5/W)**

<b>UNIT</b>	<b>DATE</b>	<b>PERIOD</b>	<b>TOPICS TO BE COVERED</b>
<b>1</b>	<b>3<sup>rd</sup> week of March,2022</b>		<b>Microprocessor (Architecture and Programming-8085-8-bit)</b>
		<b>1</b>	1.1 Introduction to Microprocessor and Microcomputer & distinguish between them.
		<b>2</b>	1.2 Concept of Address bus, Data bus, Control bus & System Bus
		<b>3</b>	1.3 General Bus structure Block diagram
		<b>4</b>	1.4 Basic Architecture of 8085 (8 bit) Microprocessor
		<b>5</b>	1.5 Signal Description (Pin diagram) of 8085 Microprocessor
		<b>6</b>	1.6 Register Organizations, Distinguish between SPR & GPR, Timing & Control Module
		<b>7</b>	Stack, Stack pointer & Stack top
		<b>8</b>	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM)
	<b>9</b>	Previous year questions discussion & assignments.	
<b>2</b>	<b>1<sup>st</sup> week of April,2022</b>		<b>Instruction Set and Assembly Language Programming</b>
		<b>1</b>	Addressing data & Differentiate between one-byte, two-byte & three-byte instructions with examples
		<b>2</b>	Addressing modes in instructions with suitable examples.
		<b>3</b>	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
		<b>4</b>	Simple Assembly Language Programming of 8085
		<b>4.a</b>	Simple Addition & Subtraction
		<b>4.b</b>	Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits
		<b>4.c</b>	Counters & Time delay (Single Register, Register Pair, More than Two Register)
		<b>4.d</b>	Looping, Counting & Indexing (Call/JMP etc)
		<b>4.e</b>	Stack & Subroutine programs
		<b>4.f</b>	Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
		<b>4.g</b>	Compare between two numbers
		<b>4.h</b>	Array Handling (Largest number & smallest number in the array)
<b>5</b>	Memory & I/O Addressing		
<b>6</b>	Previous year questions discussion & assignments.		
<b>3</b>	<b>4<sup>th</sup> week of April,2022</b>		<b>TIMING DIAGRAMS</b>
		<b>1</b>	Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.
		<b>2</b>	Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle
		<b>3</b>	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	<b>4</b>	Previous year questions discussion & assignments.	
<b>4</b>	<b>2<sup>nd</sup> week of May,2022</b>		<b>Microprocessor Based System Development Aids</b>
		<b>1</b>	Concept of interfacing
		<b>2</b>	Define Mapping & Data transfer mechanisms - Memory mapping & I/O Mapping
	<b>3</b>	Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories	

		4	Concept of Address decoding for I/O devices
		5	Programmable Peripheral Interface: 8255
		6	ADC & DAC with Interfacing.
		7	Interfacing Seven Segment Displays
		8	Generate square waves on all lines of 8255
		9	Design Interface a traffic light control system using 8255.
		10	Design interface for stepper motor control using 8255
		11	Basic concept of other Interfacing DMA controller, USART
		12	Previous year questions discussion & assignments
5	1st week of June,2022		<b>Microprocessor (Architecture and Programming-8086-16 bit)</b>
		1	Register Organisation of 8086
		2	Internal architecture of 8086
		3	Signal Description of 8086
		4	General Bus Operation& Physical Memory Organisation
		5	Minimum Mode & Timings
		6	Maximum Mode & Timings
		7	Interrupts and Interrupt Service Routines, Interrupt Cycle, Non-Maskable Interrupt, Maskable Interrupt
		8	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators,
		9	Simple Assembly language programming using 8086 instructions
		10	Previous year questions discussion & assignments
6	3 <sup>rd</sup> week of June,2022		<b>Microcontroller (Architecture and Programming-8 bit)</b>
		1	Distinguish between Microprocessor & Microcontroller
		2	8 bit & 16 bit microcontroller
		3	CISC & RISC processor
		4	Architecture of 8051Microcontroller
		5	Signal Description of 8051 Microcontrollers
		6	Memory Organisation-RAM structure, SFR
		7	Registers, timers, interrupts of 8051Microcontrollers
		8	Addressing Modes of 8051
		9	Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions , JUMP, LOOP, CALL Instructions, I/O Port Programming
		10	Interrupts, Timer & Counters
		11	Serial Communication
		12	Microcontroller Interrupts and Interfacing to 8255
		13	Previous year questions discussion & assignments